

ANALOG WAY MIDRA™ 4K

Module: INPUTS

Crestron 3-series & 4-series

Date: May 20th 2025
Driver version: V1.3.1
Compatible with: Midra™ 4K Firmware v1.1.9 or above

GENERAL

This module provides information about the inputs of the Midra™ 4K video processor as well as some commands to switch on/off HDCP compatibility.

CONTROL

General

AW_In_RefreshInfos	Digital_in	Pulse this signal to force the download and processing of information from the Midra 4K video processor. Most of the time, this signal is never used
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Inputs_status

AW_In_Available_FB[X]	Digital_out	Equals 1 when input X is available, 0 otherwise
AW_In_SignalPresence_FB[X]	Digital_out	Equals 1 when is a signal is detected on input X, 0 otherwise

AW_In_PlugUsed_FB[X]	Analog_out	The index of the plug used on input X (for multiplug inputs only)
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Hdcp

AW_In_HdcpToggle_Cmd[X]	Digital_In	Toggles HDCP for input X
AW_In_HdcpDefault_Cmd[X]	Digital_In	Forces HDCP default mode for input X
AW_In_Hdcp_FB[X]	Digital_Out	Equals 1 when HDCP is enabled for input X

In_Program

AW_In_UsedInProgram_FB[X]	Digital_Out	Equals to 1 when input X is displayed in a Screen or an AUX (Program)
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In_Preview

AW_In_UsedInPreview_FB[X]	Digital_Out	Equals to 1 when input X is displayed in a Screen or an AUX (Preview)
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Format

AW_In_SignalWidth_FB[X]	Analog_Out	The signal width in pixels for input X
AW_In_SignalHeight_FB[X]	Analog_Out	The signal height in pixels for input X
AW_In_Format_FB[X]	String_Out	The signal format for input X (syntax: Width x Height in pixels)

Label

AW_In_Label_FB[X]	String_Out	The label for Input X as configured through the Web RCS
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